

Amritanilayam Stotras

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[illegible]

The diagram illustrates a sequence of operations on a 16-bit register, organized into four main sections:

- Section 1: Initial Setup and First Operation**
 - Initial state: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
 - Operation: Shift right by 1 bit.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
- Section 2: Second Operation and Comparison**
 - Operation: Add 1 to the 16-bit register.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
 - Operation: Compare the 16-bit register with the 8-bit register.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
- Section 3: Third Operation and Shift**
 - Operation: Shift right by 1 bit.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
 - Operation: Add 1 to the 16-bit register.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
- Section 4: Final Operation and Result**
 - Operation: Shift right by 1 bit.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).
 - Operation: Add 1 to the 16-bit register.
 - Result: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).

The final result is shown in a box at the bottom right: 16-bit register (all 1s), 8-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s), 4-bit register (all 1s).

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