

Amritanilayam Stotras

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The diagram consists of several rectangles arranged in a sequence. The first row has a long rectangle on the left and a small square in the middle. The second row has a single long rectangle. The third row has a small square on the left. The fourth row has four rectangles of equal length but decreasing width from left to right. The fifth row has a short rectangle on the left, followed by a gap, then the number '2', and another short rectangle on the right.





































































































































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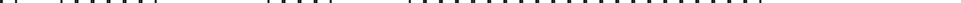









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The diagram shows three tens rods and one ten rod with seven ones units attached, totaling 37 units.





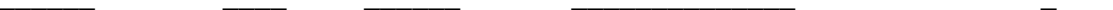







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The image shows base ten blocks. The top row consists of two tens rods and five tens rods. The bottom row consists of two tens rods, two tens rods, two tens rods, one tens rod, and one tens rod. To the right of the bottom row is a label "20" followed by a small square representing one unit.



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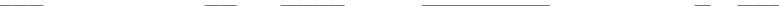








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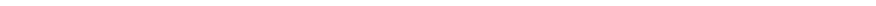
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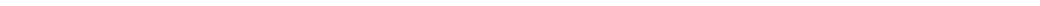
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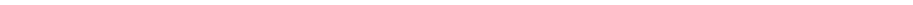









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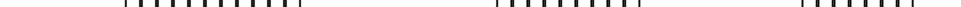










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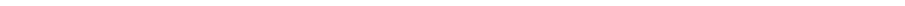






















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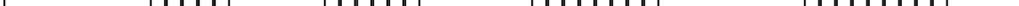









































































































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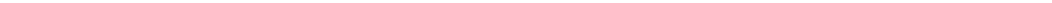









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107

Diagram illustrating the composition of the number 109 using base-ten blocks:

- Row 1: Two groups of five blocks each, representing $5 + 5$.
- Row 2: One group of ten blocks, representing 10 , followed by a closing bracket $]$.
- Row 3: One long bar representing ten blocks, followed by a space, then another group of ten blocks, a space, and finally two single blocks, representing $10 + 10 + 2 = 22$.

The total value represented is 109 units.

Diagram illustrating the padding process for a 110-bit message to fit a 128-bit frame structure. The message is split into two 56-bit segments. The first segment is padded with 10 zeros to 66 bits. The second segment is padded with 10 zeros to 66 bits. These two 66-bit segments are concatenated to form a 132-bit intermediate frame. This intermediate frame is then padded with 6 zeros to reach the final 138-bit frame size.











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